

REV	DATE	REVISION RECORD	DWN	AUTH
-	10-11-05	Draft	PAC	

**ELECTRICAL**

**Input Voltage**

Fixed between +8 VDC and +28 VDC, typical  
(Other voltages may be acceptable – consult factory)

**Output Voltages**

Fixed between +5 VDC and +25 VDC, typical  
(Other voltages may be acceptable – consult factory)

**Output Current**

Current will vary depending on voltage levels,  
differential and input voltage tolerance.

Example:

- $I_{OUT} \leq 1$  amp under the following conditions:
  - Input to Output Differential > 3 VDC
  - Input Voltage Tolerance < 5%

**MECHANICAL**

**Dimensions**

1.750 x 2.5 x 1”

**Packaging**

Nickel-plated machined aluminum

**Mounting**

Two mounting options are available. The 2.5 x 1”  
surface will offer the best thermal dissipation.

**OTHER**

**Label**

Label as follows on conventional label:

LNVR - I - O - P - C

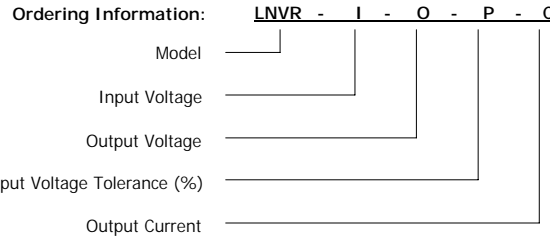
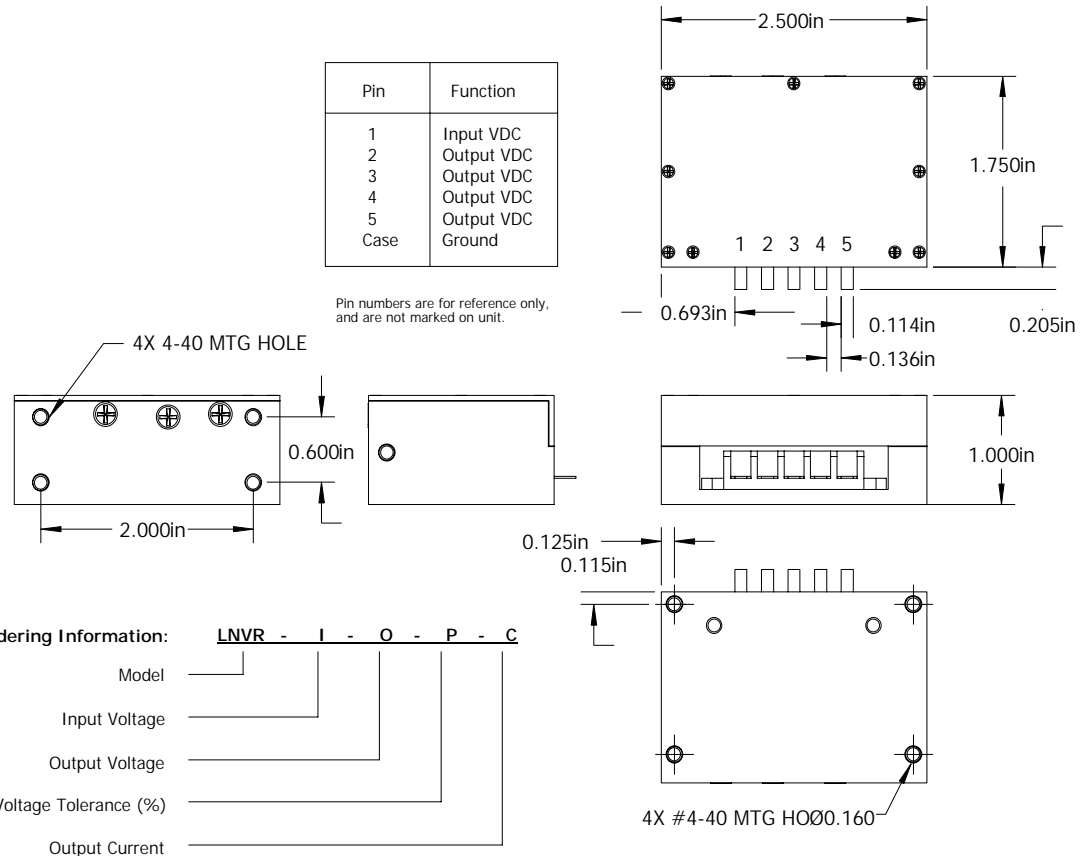
Voltage Regulator


SN – Date Code

(Replace I, O, P and C with the actual values)

Pin	Function
1	Input VDC
2	Output VDC
3	Output VDC
4	Output VDC
5	Output VDC
Case	Ground

Pin numbers are for reference only,  
and are not marked on unit.



 <b>Wenzel Associates, Inc.</b> Austin, Texas				
Title: <b>Low Noise Voltage Regulator</b>				
P/N:	Rev:	Date:	Drawn:	Ref:
<b>LNVR</b>	-	<b>10-11-05</b>		
Tolerances: (except as noted) Dimensions are in inches	0.XX Dec: <b>±0.030"</b>	0.XXX Dec: <b>±0.010"</b>	FSCM: <b>62821</b>	Page <b>1</b> of <b>1</b>