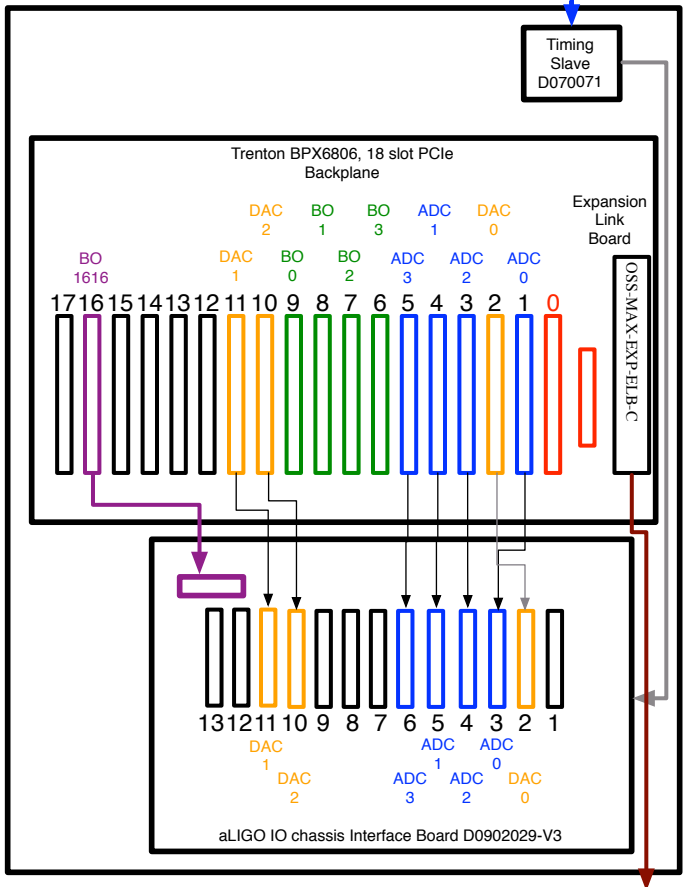


c1sus IO CHASSIS

From Timing Distribution Master



To Front End Computer